

REMARKS

Claims 1, 2, 3, 4, 17 and 24 are cancelled without prejudice.

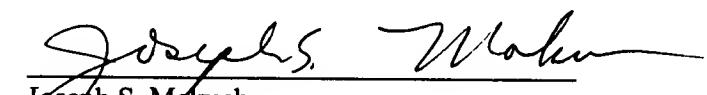
Claims 25-27 are objected to as being dependent on a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claim. Claims 25-27 have been rewritten as suggested, and are therefore, allowable.

Claims 5-16, 18-23 and 28-32 are allowed.

Applicant requests reconsideration in view of the foregoing amendments and remarks. The Examiner is encouraged to telephone the undersigned at (503) 222-3613 if it appears than an interview would be helpful in advancing the case.

Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

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In the Claims:

25. (Amended once) A memory system [according to claim 24 further] comprising:
a bus;
a plurality of stacks of memory devices;
a buffer coupled between the plurality of stacks of memory devices and the bus;
a second plurality of stacks of memory devices; and
a second buffer coupled between the second plurality of stacks of memory devices and the bus.

26. (Amended once) A memory system [according to claim 24 further] comprising:

a bus;
a plurality of stacks of memory devices;
a buffer coupled between the plurality of stacks of memory devices and the bus;
a second plurality of stacks of memory devices; and
a second buffer coupled between the second plurality of stacks of memory devices and the first buffer.

27. (Amended once) A memory system [according to claim 24] comprising:
a bus;
a plurality of stacks of memory devices; and
a buffer coupled between the plurality of stacks of memory devices and the bus;
wherein the buffer is adapted to receive a signal from the bus and redrive the signal to another buffer.

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